CMPE 310 Hardware Project Checklist

Due: Friday, Dec. 1

Hardcopy

- ✓ Project Report
- ✓ Full schematic
- ✓ Bill of Materials
- ✓ Each layer of PCB layout
 - o TOP
 - o BOTTOM
 - o POWER
 - o GND
 - o Silkscreen Top (SST)
 - o Drill Drawing (DRD)

Softcopy

Submit softcopy using **submit** command, the class name is **cmpe310** and the project name is **final_proj**.

- ✓ Capture CIS files zipped or tarred into one file called schematic
 - o Project file (.opj)
 - o Database file (.DSN)
- ✓ Gerber files from Layout Plus zipped or tarred into one file called layout
 - o Use **Extended Gerber** (RS-274X) format
 - o Include the following files
 - .TOP
 - .BOT
 - .GND
 - .PWR
 - .SST
 - .AST
 - .DRD
 - .FAB